Hrishikesh Venkatesh

J +44 7913 626854 ▼ hv122@ic.ac.uk 🛅 linkedin.com/in/hrishi-v/ 🗘 github.com/hrishi-v 🛕 hrishi-v.github.io

Education

Imperial College London

Oct 2022 - Jun 2026

Masters in Electronic and Information Engineering

Projected First

Completed Modules: Machine Learning (83), Deep Learning (80), Digital System Design (80).

Current Modules: Hardware and Software Verification, Program Analysis

Heckmondwike Grammar School

Sep 2020 - Jun 2022

A-Levels

A*A*AA in Further Maths, Maths, Physics and Chemistry.

Recent Experience

Apple Jun - Sep 2025

Framebuffer Driver Engineer, Intern

- Worked on large C/C++ display driver codebases, within a *nix kernel.

- Used in-house performance engineering tools to trace and instrument kernel processes for performance.
- Implemented an algorithm to minimise display driver DMA bandwidth, achieving an 87% reduction.
- Leveraged knowledge of *nix, C++, kernel drivers and scripting.

HSBC Jun - Aug 2024

Software Engineer, Intern

- Refactored a server certificate tracking program, resulting in an 87% reduction in LOC using Pandas.
- Automated an existing process within change management, reducing employee time requirement from 10 minutes to 1.
- Leveraged knowledge of Python, Pandas, Batch scripts and SQL.

Extra-curricular Roles

Imperial College Data Science Society

Jun 2025 - Jun 2026

Chair

- Responsible for developing lectures and resources to teach other members of the society about data science.
- Led the societies' most successful year to date, increasing student interest by 10%.

Imperial College London

Oct 2024 - Dec 2025

Undergraduate Teaching Assistant

- Worked as part of the Instruction Architectures and Compilers module.
- Tested and updated lab instructions for the latest Ubuntu and WSL versions.
- **Utilised** SystemVerilog, C++ and Linux knowledge.

Projects

System Performance Engineering Coursework

Jan - Mar 2025

- Profiled Tablesaw Java code using VTune, within a Singularity VM.
- Scored a **high first-class** in the coursework.

Digital System Design Coursework

Jan - Mar 2025

- Made a pipelined implementation of the CORDIC algorithm.
- Achieved a high-first class in the module, with a **10th percentile** execution speed.

C90 Compiler Jan - Mar 2024

- Developed the arithmetic and type compilation, covering ints, floats, doubles, chars and strings.
- Passed all seen test cases, as well as 84% overall, one of the highest scores in the cohort.

Technical Competencies

Automation Bash, YAML, Batch, Python Languages C++, SystemVerilog, JavaScript, Java Data Science PyTorch, Tensorflow, Pandas Technologies Git, Unix, Linux, VTune